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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Shunpei Yamazaki, et al. Art Unit: 1792

Serial No.: 09/892,225 Examiner: Mathew J. Song

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Title : SEMICONDUCTOR DEVICE AND FABRICATION METHOD THEREFOR

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: October 30, 2007

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